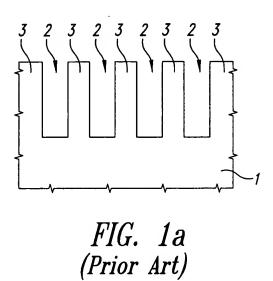
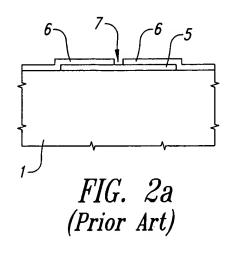
Title: PROCESS FOR FORMING A BURIED CAVITY IN A SEMICONDUCTOR MATERIAL WAFER AND A BURIED CAVITY Express Mail No. EV336615168US

Inventor(s): Pietro Erratico et al.

Docket No. 854063.618D1





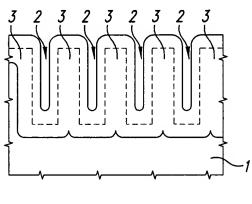
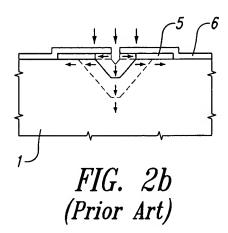


FIG. 1b (Prior Art)



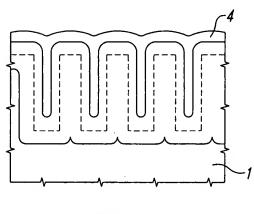
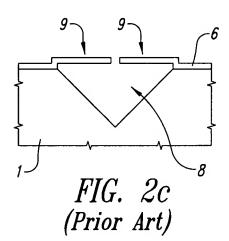


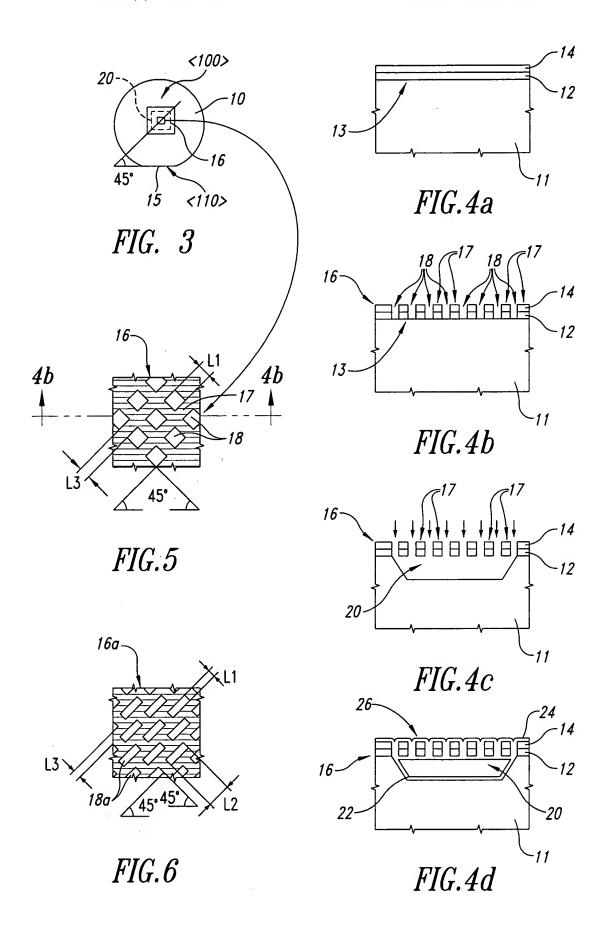
FIG. 1c (Prior Art)



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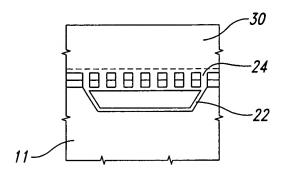


FIG. 7a

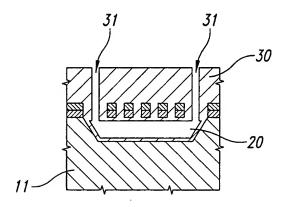


FIG. 7b